

REMARKS

Claims 1-5 and 8 remain pending in the present application. The Applicants respectfully request reexamination of the present application in view of the following comments.

Allowable Matter

The Official Action indicates that Claim 5 would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Applicants thank the Examiner for indicating allowable material.

35 U.S.C. § 102

Claims 1-5 and 8 stand rejected under 35 USC § 102(b) as being allegedly unpatentable over Matthies. (US 6,498,592, "Matthies"). Applicants have reviewed the cited reference and respectfully assert that embodiments in accordance with the present invention as recited in Claims 1-5 and 8 are patentable over Matthies.

Matthies is classified in class 345/1.1, “computer graphics processing and selective visual display systems – plural display systems.” Applicants respectfully assert that the Matthies reference bears no relation to the field of art of the present invention, and thus fails to anticipate any claimed embodiments of the present invention.

Applicants respectfully assert that Matthies fails to teach or suggest in any way the claimed limitation of “a method of generating a deep N-well pattern for an integrated circuit design,” as recited by Claim 1. Moreover, Matthies fails to suggest anything about the claimed limitation of an “integrated circuit design.” For example, Matthies mentions the term “integrated circuit” only twice, in the same paragraph, to teach that an integrated circuit may be mounted on a circuit board (column 4 lines 45-50). Applicants respectfully assert that this teaching, of mounting an integrated circuit to a circuit board, fails to teach or suggest anything about the claimed limitation of “an integrated circuit design,” as recited by Claim 1.

For this reason, Applicants respectfully assert that Claim 1 overcomes the rejections of record, and respectfully solicit allowance of this Claim.

Moreover, Applicants respectfully assert that Matthies fails to teach or suggest the claimed limitation of “a deep N-well pattern (for an integrated

circuit),” as recited by Claim 1. While Matthies may teach some patterning for constructing a display, such patterning has nothing to do with integrated circuits and further has nothing to do with “deep N-well pattern(s)” for integrated circuits.

For this further reason, Applicants respectfully assert that Claim 1 overcomes the rejections of record, and respectfully solicit allowance of this Claim.

Applicants respectfully assert that Claims 2-5 and 8 overcome the rejections of record by virtue of their dependence, and respectfully solicit allowance of these Claims.

CONCLUSION

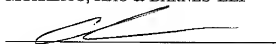
Claims 1-5 and 8 remain pending in the present application. The Applicants respectfully request reexamination of the present application in view of the remarks presented herein.

The Examiner is invited to contact Applicants' undersigned representative if the Examiner believes such action would expedite resolution of the present Application. Please charge and additional fees or apply any credits to our PTO deposit account number: 50-4160

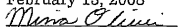
Respectfully submitted,

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Date: 2/13/2008


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I hereby certify that this correspondence is being electronically transmitted to the USPTO via EFS on the date shown below:
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